

SuperMOS – PDFN5*6-8L 100V BV_{DSS}, 91mΩ R_{DS(ON)}, N-channel MOSFET

1. Description

The ESN6484 is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product ESN6484 is Pb-free.

2. Features

- 100V, R_{DS(ON)}=91mΩ(Tpy), V_{GS}=10V
R_{DS(ON)}=97mΩ(Tpy), V_{GS}=4.5V
 - Use trench MOSFET technology
 - High density cell design for low R_{DS(on)}
- Material: Halogen free
 - Reliable and rugged
 - Avalanche Rated
 - Low leakage current

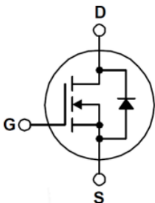
3. Applications

- PWM applications
 - Load switch
 - Power management in portable/desktop PCs
 - DC/DC conversion
- 100% UIS TESTED!

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
ESN6484	PDFN5*6-8L	ESN6484/lot	Halogen free	Tape & Reel	5,000 PCS	UL 94V-0	13 inches

5. Pin Configuration and Functions

Pin	Function	Outline	Circuit Diagram
4	Gate		
1/2/3	Drain		
5/6/7/8	Source		

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		BV_{DSS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C=25^{\circ}\text{C}$	I_D	11	A
	$T_C=75^{\circ}\text{C}$		9	
Maximum Power Dissipation	$T_C=25^{\circ}\text{C}$	P_D	25	W
	$T_C=75^{\circ}\text{C}$		15	
Pulsed Drain Current ^a		I_{DM}	44	A
Avalanche Current, Single Pulsed ^b		I_{AS}	12.5	A
Avalanche Energy, Single Pulsed ^b		E_{AS}	23.4	mJ
Operating Junction Temperature		T_J	150	$^{\circ}\text{C}$
Lead Temperature		T_L	260	$^{\circ}\text{C}$
Storage Temperature Range		T_{stg}	-55 to 150	$^{\circ}\text{C}$

Thermal resistance ratings

Single Operation					
Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance	$t \leq 10 \text{ s}$	$R_{\theta JA}$	21	25	$^{\circ}\text{C/W}$
Junction-to-Case Thermal Resistance ^c	Steady State	$R_{\theta JC}$	3.5	5	

Note:

a: Repetitive rating, pulse width limited by junction temperature, $t_p=10\mu\text{s}$, Duty Cycle=1%

b: EAS condition: $T_J=25^{\circ}\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $L=0.3\text{mH}$, $R_g=25\Omega$

c: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	uA
Gate-to-source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	1.0	1.8	3.0	V
Drain-to-source On-resistance	R _{DS(on)}	V _{GS} =10V, I _D =7.5A		91	122	mΩ
		V _{GS} =4.5V, I _D =5A		97	142	
Forward Trans conductance	g _{FS}	V _{DS} =5.0V, I _D =7.5A			40	S
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C _{ISS}	V _{GS} =0V, f=1MHz, V _{DS} =50V		780		pF
Output Capacitance	C _{OSS}			58		
Reverse Transfer Capacitance	C _{RSS}			26		
Total Gate Charge	Q _{G(TOT)}	V _{GS} =4.5V, V _{DS} =50V, I _D =7.5A		19.2		nC
Gate-to-Source Charge	Q _{GS}			3.5		
Gate-to-Drain Charge	Q _{GD}			5.3		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DS} =50V, R _L =6Ω, R _G =3Ω		6		ns
Rise Time	t _r			2.6		
Turn-Off Delay Time	t _{d(OFF)}			22		
Fall Time	t _f			2.5		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} =0V, I _S =1A		0.7	1.2	V

7. Typical Characteristic

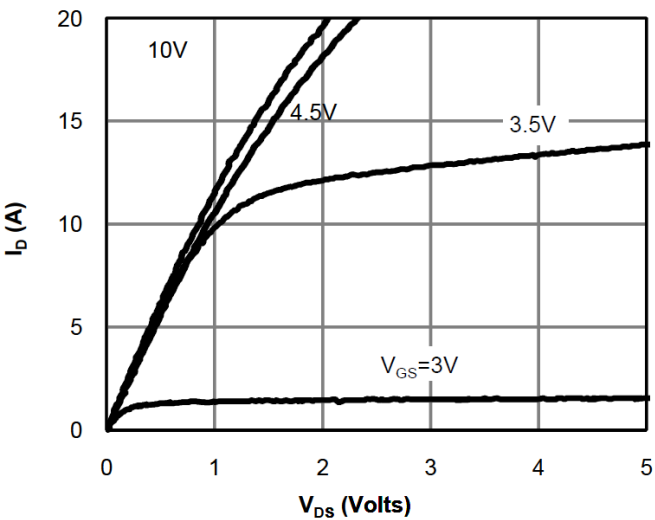


Fig 1: On-Region Characteristics

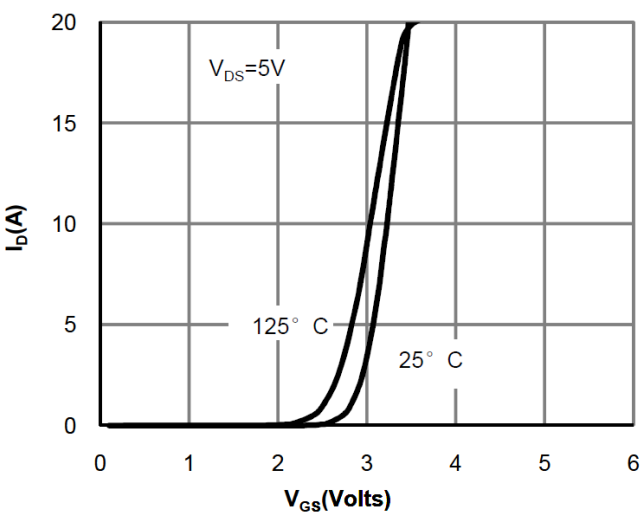


Figure 2: Transfer Characteristics

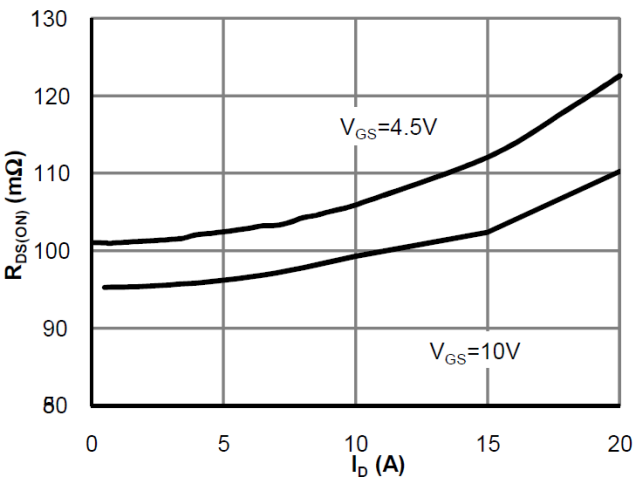


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

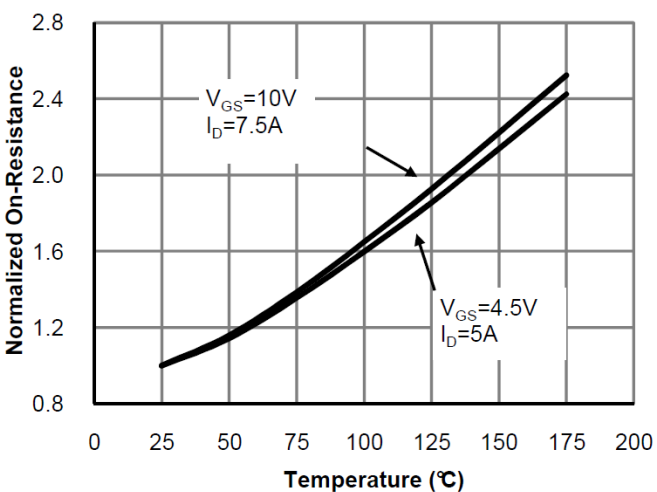


Figure 4: On-Resistance vs. Junction Temperature

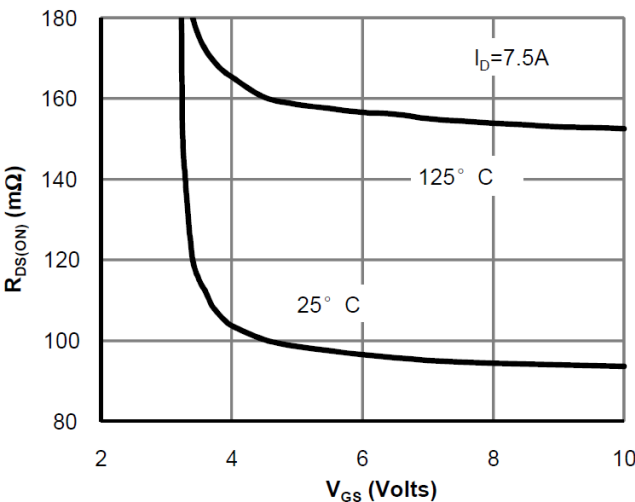


Figure 5: On-Resistance vs. Gate-Source Voltage

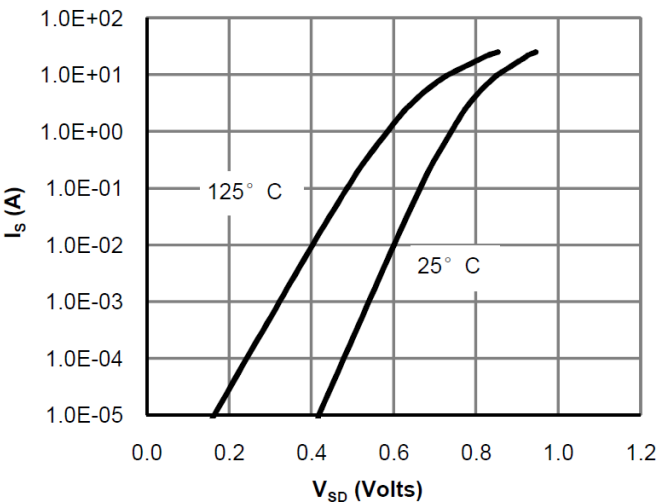


Figure 6: Body-Diode Characteristics

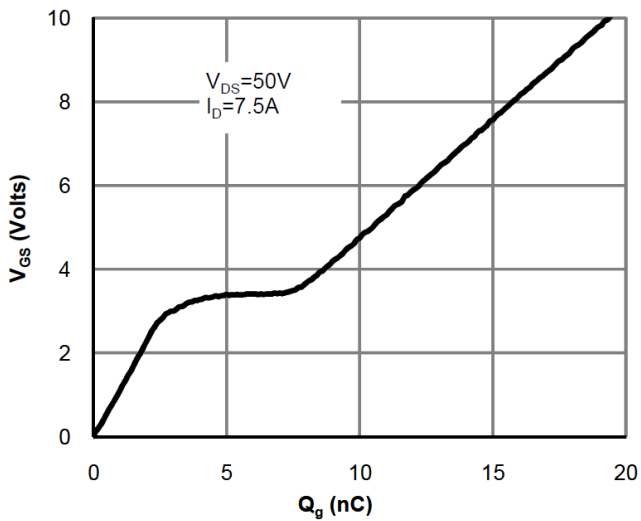


Figure 7: Gate-Charge Characteristics

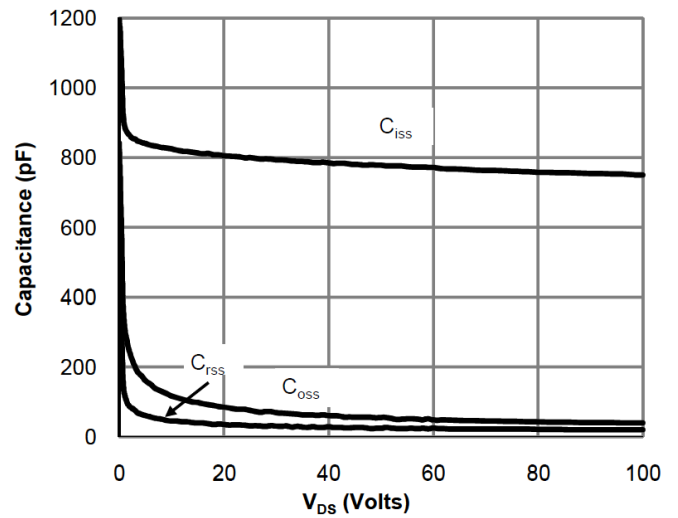


Figure 8: Capacitance Characteristics

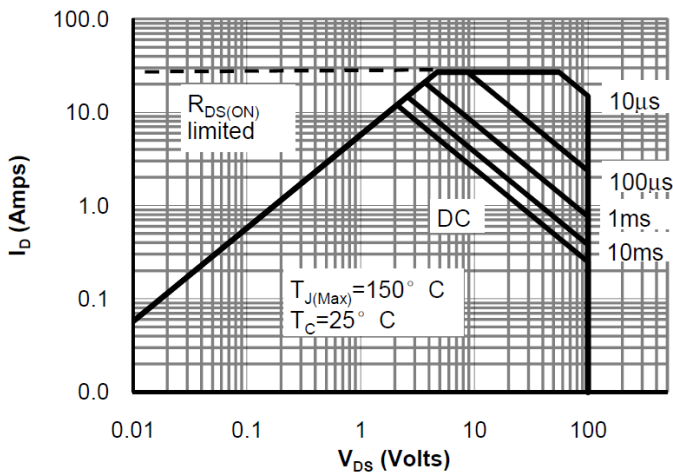


Figure 9: Maximum Forward Biased Safe Operating Area

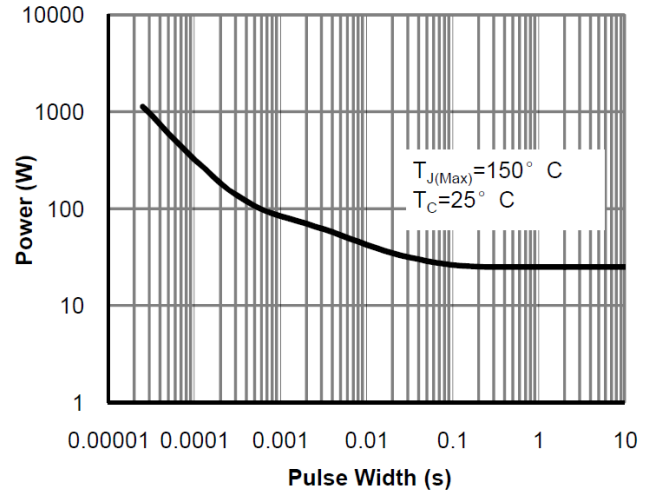


Figure 10: Single Pulse Power Rating Junction-to-Case

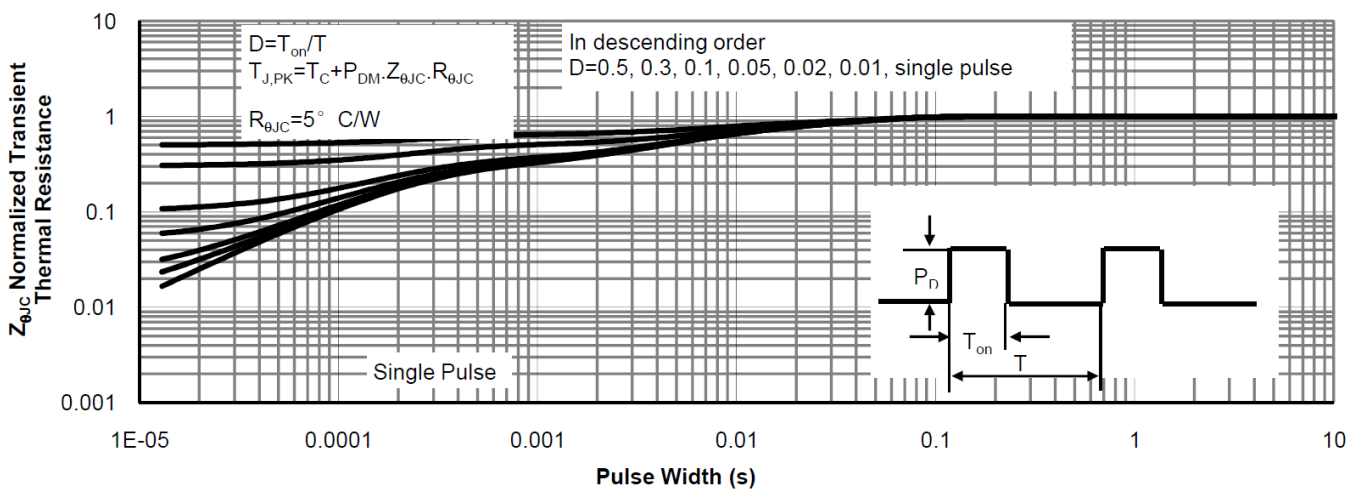
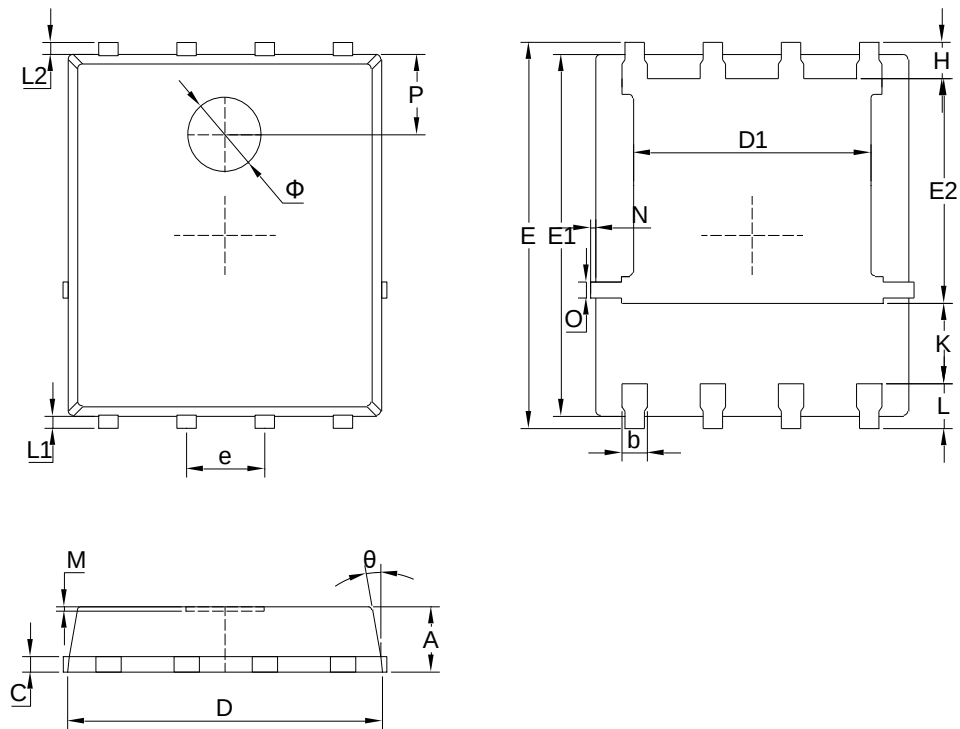


Figure 11: Normalized Maximum Transient Thermal Impedance

8. Dimension (PDFN5*6-8L)



Symbol	Dimensions in Millimeters			Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX		MIN	NOM	MAX
A	0.90	1.05	1.20	H	0.48	0.58	0.68
b	0.35	0.40	0.50	K	1.17	1.27	1.37
C	0.20	0.25	0.35	L	0.64	0.74	0.84
D	4.90	5.05	5.20	L1/L2	0.20 REF.		
D1	3.72	3.82	3.92	θ	8°	10°	12°
E	6.00	6.15	6.30	M	0.08 REF.		
E1	5.60	5.75	5.90	N	0	-	0.15
E2	3.47	3.57	3.67	O	0.25 REF.		
e	1.27 BSC.			P	1.28 REF.		
Φ	1.20 REF						

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