

SOT-23 Plastic-Encapsulate MOSFETS

Features

- VDS= -60V
- ID=-1.25A
- RDS(on)@VGS= -10V < 340mΩ
- RDS(on)@VGS= -4.5V < 550mΩ
- Trench Power MV MOSFET technology
- Voltage controlled small signal switch
- Fast Switching Speed

Applications

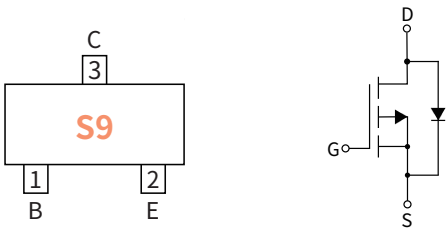
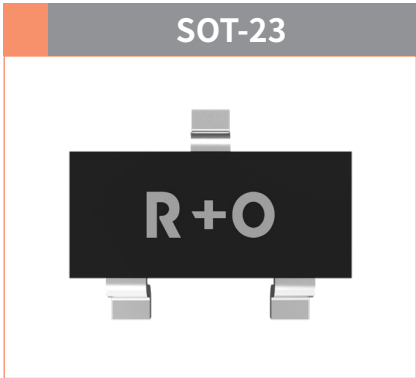
- Battery operated systems
- Solid-state relays
- Direct logic-level interface: TTL/CMOS

Mechanical Data

- Case: SOT-23
Molding compound meets UL 94V-0 flammability rating, RoHS-compliant,halogen-free
- Terminals: Solder plated, solderable per MIL-STD-750,Method 2026

Reference News

Drain-source Voltage
-60 V
Drain Current
-1.25 Ampere



Maximum Ratings (Ta=25°C Unless otherwise specified)

Table with 4 columns: PARAMETER, SYMBOL, UNIT, VALUE. Rows include Drain-source Voltage, Gate-source Voltage, Drain Current, Pulsed Drain Current, Total Power Dissipation, Thermal Resistance, and Junction and Storage Temperature Range.

Note :
(1)Surface Mounted on FR4 Board.
(2) t ≤ 5 sec.

Ordering Information

Table with 7 columns: PACKAGE, PACKAGE CODE, UNIT WEIGHT(g), REEL(pcs), BOX(pcs), CARTON(pcs), DELIVERY MODE. Row for SOT-23 package.

● Static Parameter Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	V	-60	—	—
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-48V, V_{GS}=0V$	μA	—	—	-1.0
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	nA	—	—	± 100
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	V	-1.0	—	-3.0
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-1.25A$	m Ω	—	275	340
		$V_{GS}=-4.5V, I_D=-1.0A$		—	406	550
Forward Transconductance	g_{fs}	$V_{DS}=-4.5V, I_D=-1.0A$	S	—	1.9	—
Diode Forward Voltage	V_{SD}	$I_S=-1.25A, V_{GS}=0V$	V	—	-0.82	-1.2
Maximum Body-Diode Continuous Current	I_S	—	A	—	—	-1.25

● Dynamic Parameters (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Input Capacitance	C_{iss}	$V_{DS}=-30V$ $V_{GS}=-10V$ $f=1MHz$	pF	—	380	—
Output Capacitance	C_{oss}			—	32	—
Reverse Transfer Capacitance	C_{rss}			—	6.0	—

● Switching Parameters (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Total Gate Charge	Q _g	V _{GS} = -10V V _{DS} = -30V I _D = -1.25A	nC	—	5.4	12
Gate-Source Charge	Q _{gs}			—	1.15	—
Gate-Drain Charge	Q _{gd}			—	0.92	—
Reverse Recovery Time	t _{rr}	I _F = -1.25A, di/dt=100A/μs	ns	—	30	55
Turn-on Delay Time	t _{D(on)}	V _{GS} = -4.5V, V _{DS} = -30V R _L =3.0Ω, R _{GEN} =6.0Ω I _D = -1.0A		—	10.5	20
Turn-on Rise Time	t _r			—	11.5	20
Turn-off Delay Time	t _{D(off)}			—	15.5	30
Turn-off fall Time	t _f			—	7.5	15

● Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)

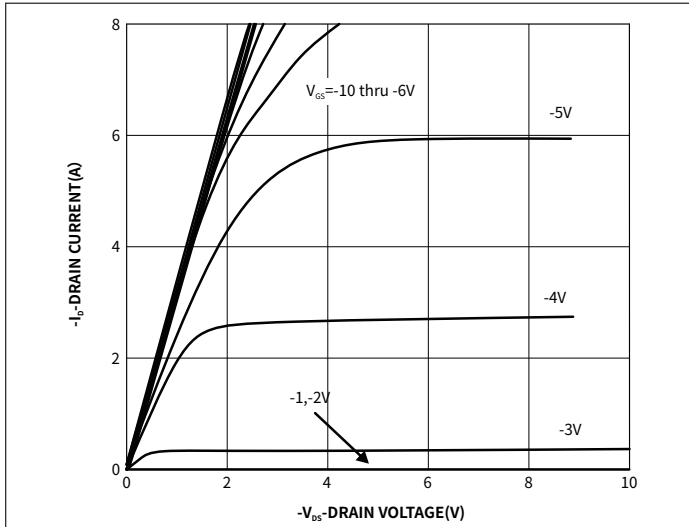


Fig.1 Output Characteristics

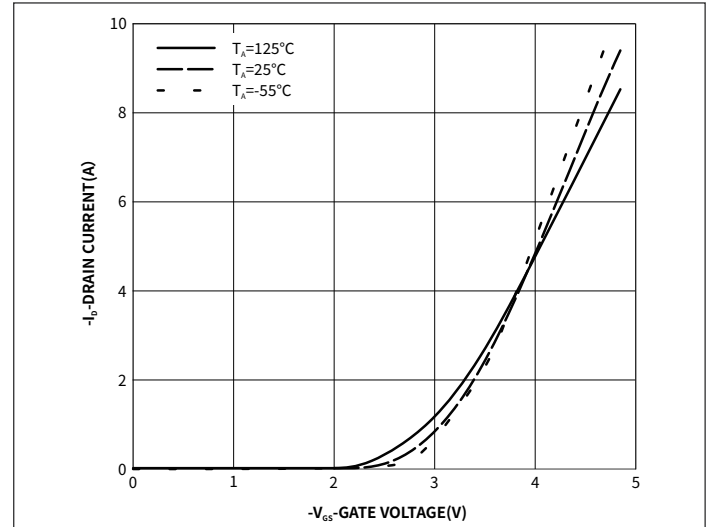


Fig.2 Transfer Characteristics

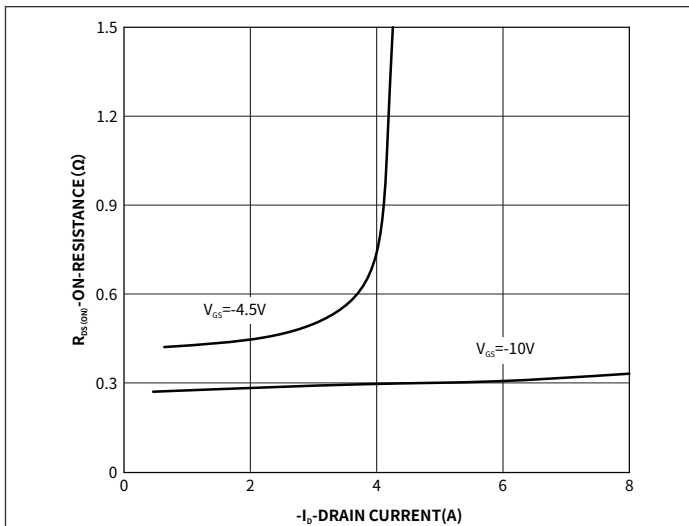


Fig.3 On-Resistance vs. Drain Current and Gate Voltage

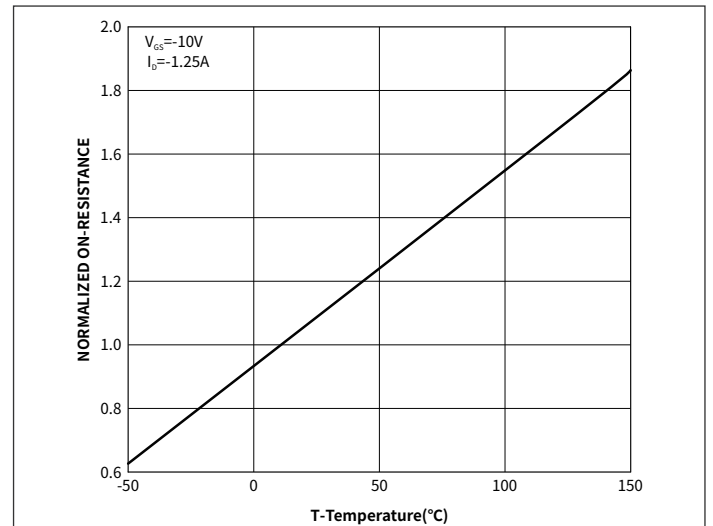


Fig.4 On-Resistance vs. Junction Temperature

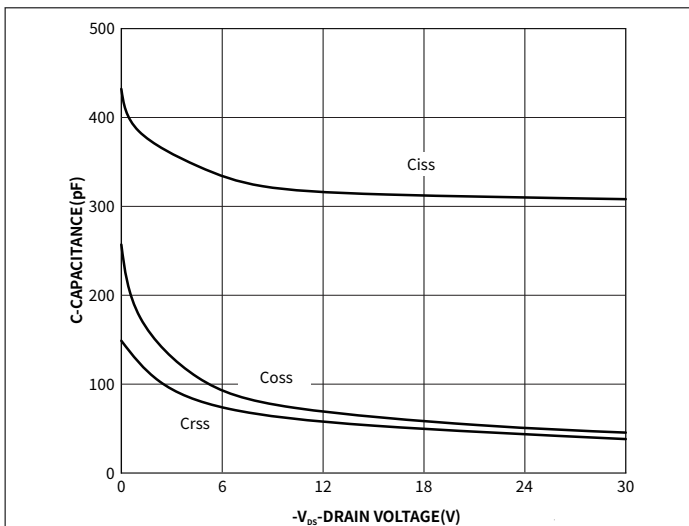


Fig.5 Capacitance Characteristics

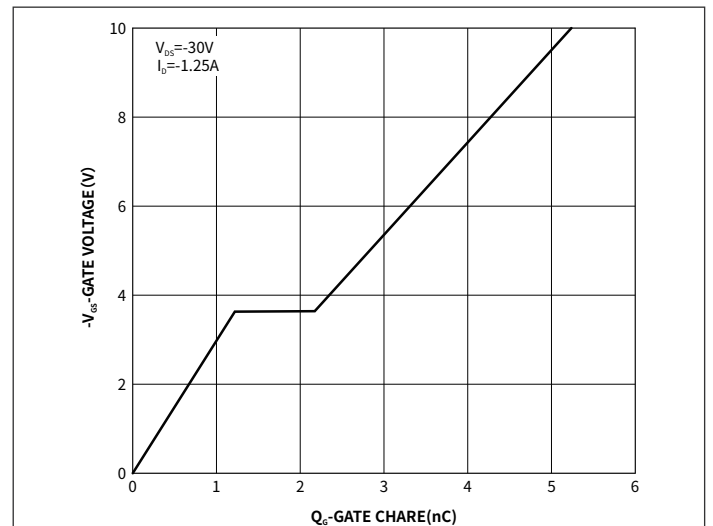
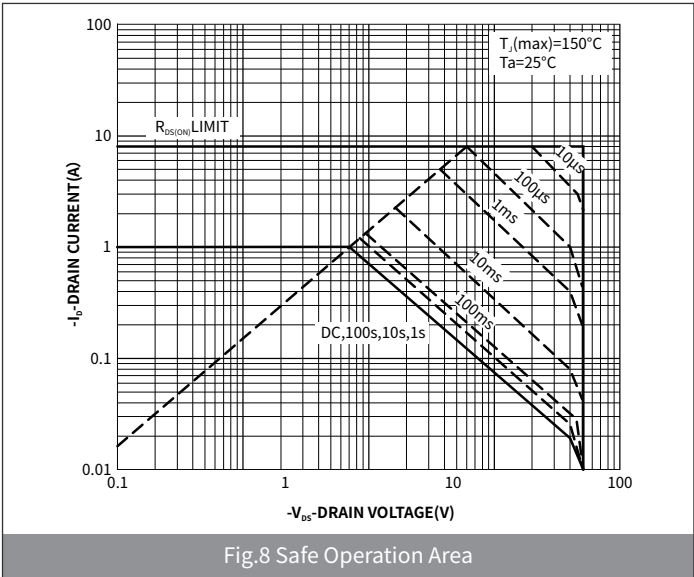
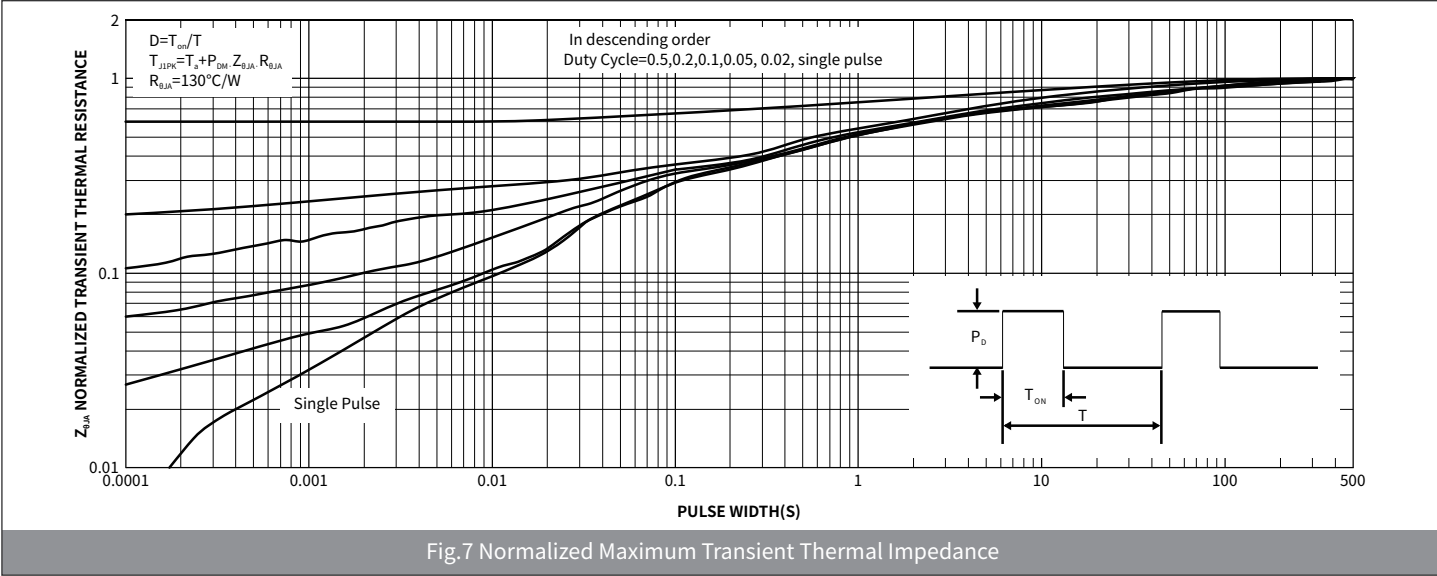
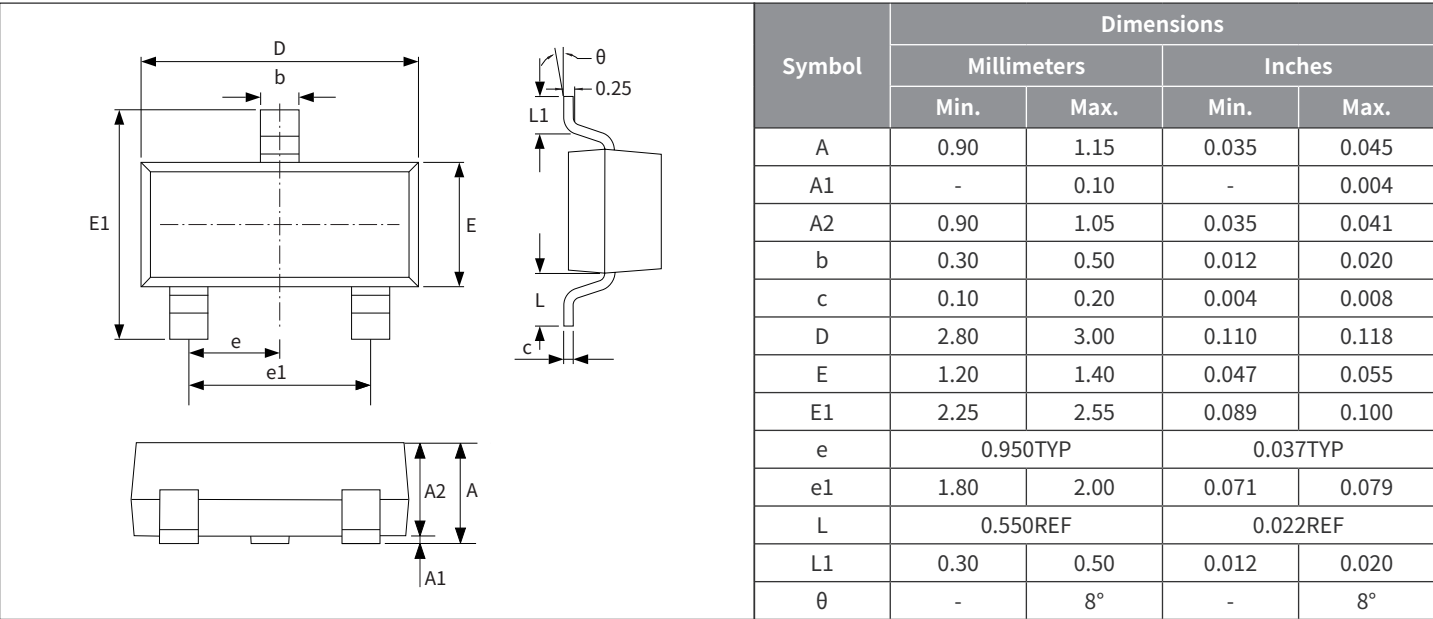


Fig.6 Gate Charge

● Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)



● Package Outline Dimensions (SOT-23)



● Suggested Pad Layout

